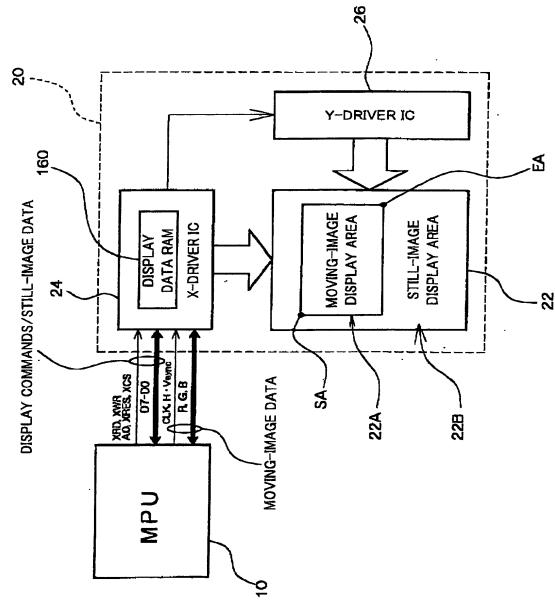
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FIG. 1



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FIG. 2

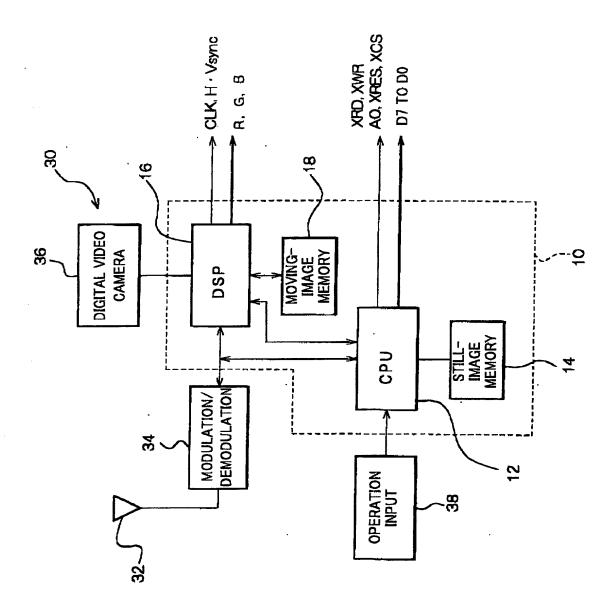


FIG. 3

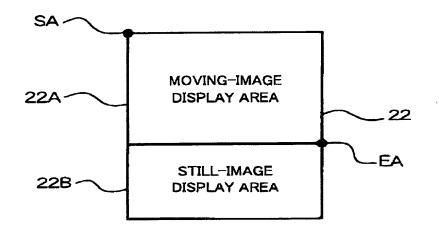
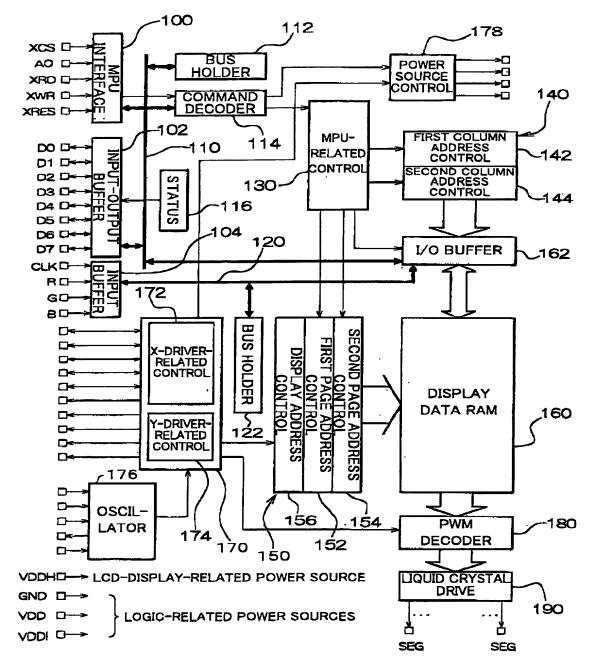


FIG. 4

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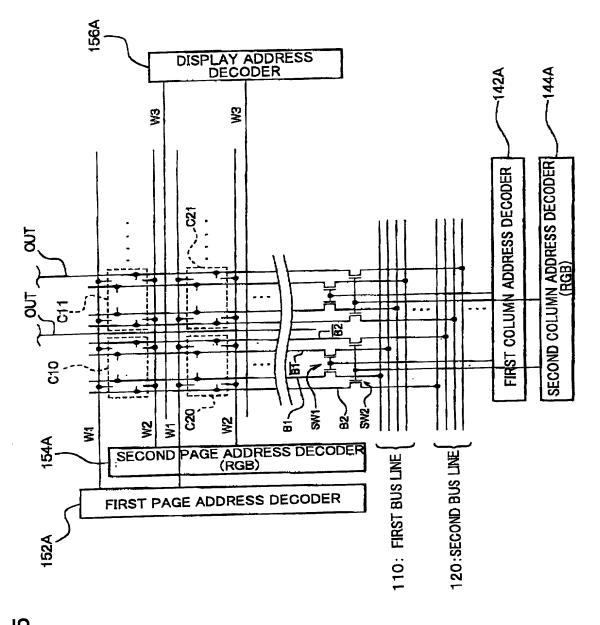


FIG. 5



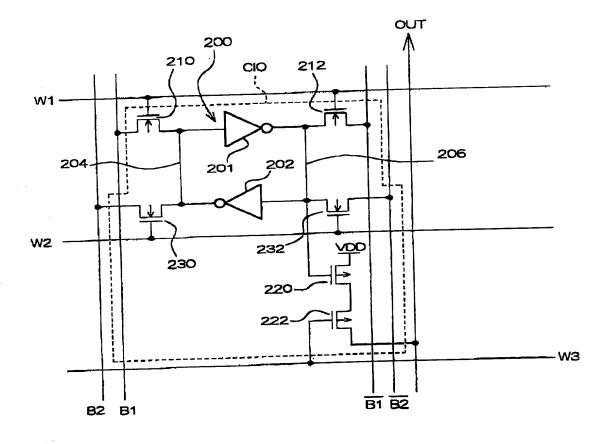
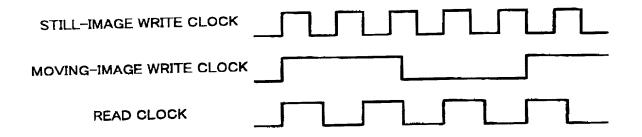


FIG. 7

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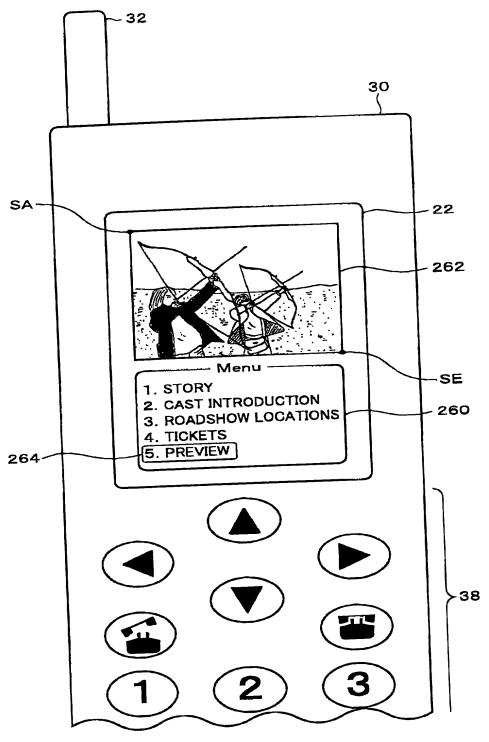
24 1<u>60</u> **DISPLAY DATA X-DRIVER IC** RAM AO, XRES, XCS CLK, H-Vsync XRD, XWR D7 T0 D0 R, G, B б б 18 9 -250 STILL-IMAGE MEMORY 4 MEMORY DIGITAL VIDEO ဓမ္မ MOVING-IMAGE MEMORY CAMERA - 254 -256 252 DSP MOVING-IMAGE DATA WRITE MOVING-IMAGE AREA SETTING STILL-IMAGE AREA SETTING STILL-IMAGE DATA WRITE CPU 258 DEMODULATION MODULATION/ 8 12 **OPERATION** INPUT 88 FIG. 8 32/

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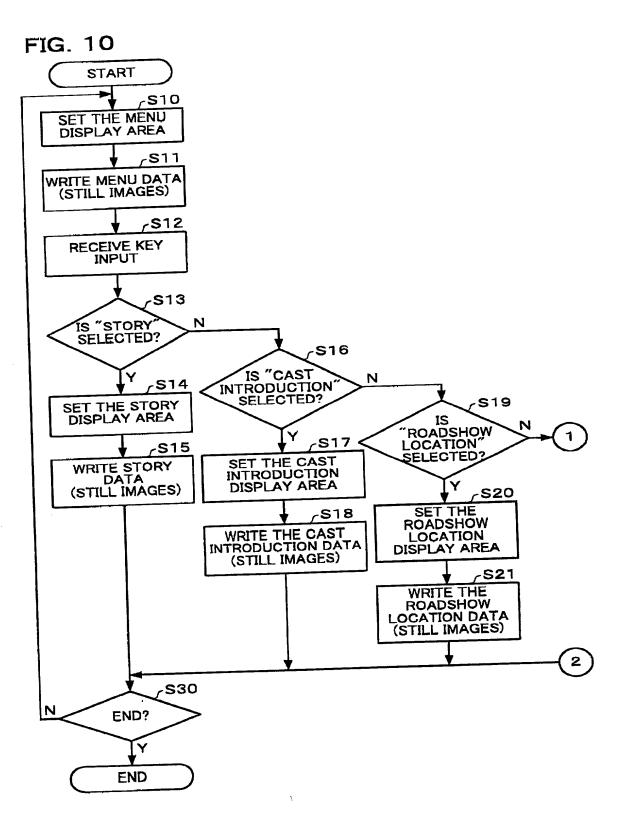


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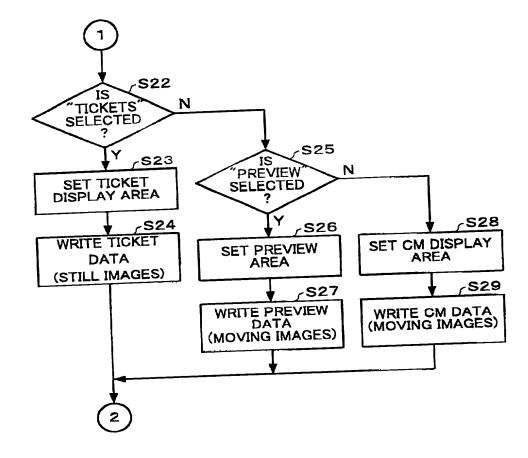
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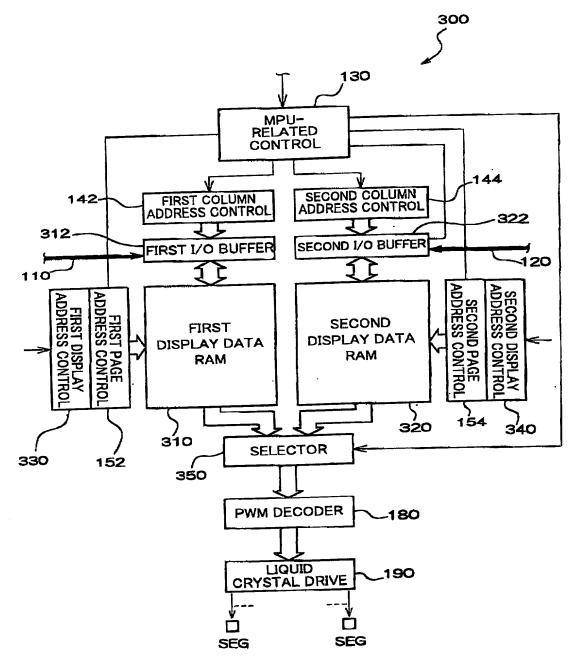
FIG. 11



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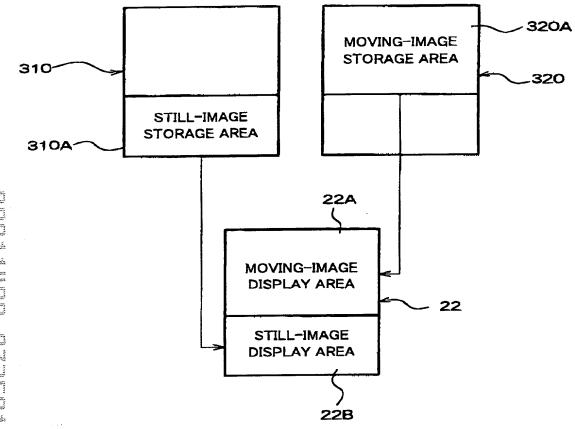
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FIG. 13



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